

REMARKS

With respect to the species election requirement contained in the Office Action, Applicant elects Invention 1 (species of Figures 2A-2I). Claims 21-40 read on the elected Invention 1. Non elected claims 61-77 have been canceled from the present application, and are being filed in a separate divisional application.

With respect to the subspecies election requirement, Applicant elects subspecies 1 (contact of Figure 2F). Claims 21-36 and 39-40 read on the elected subspecies. Should independent claim 32 be allowed, then rejoinder of non elected claims 37 and 38 will be requested.

The elected claims are directed to a method for testing a semiconductor component 10A (Figure 2A) having a plurality of terminal contacts 12A (Figure 2A). The method includes the step of providing a board 18 (Figure 2A) comprising a plurality of contacts 20 (Figure 2A) in electrical communication with test circuitry 22 (Figure 2A). The method also includes the step of providing a substrate 26 (Figure 2F) having a plurality of movable test contactors 32 (Figure 2F) comprising first contacts 34 (Figure 2F) including first conductive polymer layers 44 (Figure 2F) configured to electrically engage the terminal contacts 12A (Figure 2C), and second contacts 38 (Figure 2F) including second conductive polymer layers 46 (Figure 2F) in electrical communication with the first contacts 34 (Figure 2F) and configured to electrically engage the contacts 20 (Figure 2F).

The method also includes the steps of placing the component 10A (Figure 2A) on the substrate 26 (Figure 2F) with the terminal contacts 12A (Figure 2C) in electrical communication with the first contacts 34 (Figure 2C) and

the second contacts 38 (Figure 2C) in electrical communication with the contacts 20 (Figure 2F). The method also includes the step of applying test signals through the test contactors 32 (Figure 2F) and the terminal contacts 12A (Figure 2C) to the component 10A (Figure 2A).

Independent claim 21 has been amended to correct an informality. In addition, the Cross Reference To Related Applications has been amended to include a patent number for the issued parent case. An IDS is also being filed citing the issued parent case.

Favorable consideration and allowance of claims 21-40 is respectfully requested. Should any issues arise that will advance this case to allowance, the Examiner is asked to contact the undersigned by telephone.

DATED this 13th day of June, 2005.

Respectfully submitted:



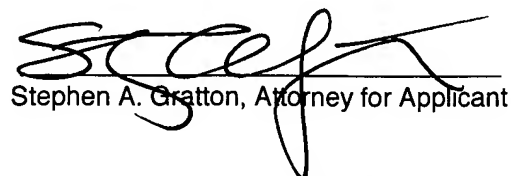
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